

Amendment to the Specification:

The Paragraph beginning at Page 1, lines 7 - 25, is to be deleted and replaced with the Paragraph as follows:

CO-PENDING APPLICATIONS

Various methods, systems and apparatus relating to the present invention are disclosed in the following co-pending applications filed by the applicant or assignee of the present invention simultaneously with the present application:

10/815621	7243835	10/815630	10/815637	10/815638	7251050
10/815642	7097094	7137549	10/815618	7156292	10/815635
10/815647	7137566	7131596	7128265	7207485	7197374
7175089	10/815617	10/815620	7178719	10/815613	7207483
7296737	7270266	10/815614	10/815636	7128270	10/815609
7150398	7159777	10/815610	7188769	7097106	7070110
7243849	7204941	7282164	10/815628		

The disclosures of these co-pending applications are incorporated herein by cross-reference.

The Paragraph beginning at Page 1 through to 3, lines 25 - 11, is to be deleted and replaced with the Paragraph as follows:

CROSS-REFERENCES

Various methods, systems and apparatus relating to the present invention are disclosed in the following co-pending applications filed by the applicant or assignee of the present invention. The disclosures of all of these co-pending applications are incorporated herein by cross-reference.

The disclosures of all of these co-pending applications and granted patents are incorporated herein by cross-reference.

7156289	7178718	7225979	09/575197	7079712	6825945
7330974	6813039	7190474	6987506	6824044	6980318
6816274	7102772	09/575186	6681045	6678499	6679420
6963845	6976220	6728000	7110126	7173722	6976035
6813558	6766942	6965454	6995859	7088459	6720985

7286113	6922779	6978019	6847883	7131058	7295839
09/607843	09/693690	6959298	6973450	7150404	6965882
7233924	09/575181	09/722174	7175079	7162259	6718061
10/291523	10/291471	7012710	6825956	10/291481	7222098
10/291825	7263508	7031010	6972864	6862105	7009738
6989911	6982807	10/291576	6829387	6714678	6644545
6609653	6651879	10/291555	7293240	10/291592	10/291542
7044363	7004390	6867880	7034953	6987581	7216224
10/291821	7162269	7162222	7290210	7293233	7293234
6850931	6865570	6847961	10/685523	10/685583	7162442
10/685584	7159784	10/804034	10/793933	7068382	7007851
6957921	6457883	10/743671	7094910	7091344	7122685
7038066	7099019	7062651	6789194	6789191	6644642
6502614	6622999	6669385	6827116	6549935	6987573
6727996	6591884	6439706	6760119	7295332	7064851
6826547	6290349	6428155	6785016	6831682	6741871
6927871	6980306	6965439	6840606	7036918	6977746
6970264	7068389	7093991	7190491	6982798	6870966
6822639	6474888	6627870	6724374	6788982	7263270
6788293	6946672	6737591	7091960	09/693514	6792165
7105753	6795593	6980704	6768821	7132612	7041916
6797895	7015901	7289882	7148644	10/778056	10/778058
10/778060	10/778059	10/778063	10/778062	10/778061	10/778057
7055739	7233320	6830196	6832717	7182247	7082562
6843420	10/291718	6789731	7057608	6766944	6766945
7289103	10/291559	7299969	10/409864	7108192	7111791
10/786631	10/683151	10/683040	10/778090	6957768	09/575172
7170499	7106888	7123239	6982701	6982703	7227527
6786397	6947027	6975299	7139431	7048178	7118025
6839053	7015900	7010147	7133557	6914593	10/291546
6454482	6808330	6527365	6474773	6550997	7093923
6957923	7131724				

BACKGROUND

The Paragraph beginning at Page 31 lines 1 - 2, is to be deleted and replaced with the Paragraph as follows:

Figure 6 is a plan view showing a relationship between a set of the tags shown in Figure 5a and a field of view of a netpage sensing device in the form of a netpage pen;

The Paragraph beginning at Page 104 lines 28 - 32, is to be deleted and replaced with the Paragraph as follows:

The imaging unit incorporates both the image sensor 2412 and the image processor 2410, which are usefully combined into a single compact chip as described in the co-pending US applications USSN 10/778,056 entitled “Image Sensor with Digital Frame Store”, USSN 10/778,058 entitled “Image Sensor with Low-Pass Filter”, USSN 10/778,060 entitled “Image Sensor with Range Expander”, USSN 10/778,059 entitled “Pixel Sensor”, USSN 10/778,063 entitled “Image Sensor for Timing Circuit”, USSN 10/778,062 entitled “Image Processor with Low Power Mode”, USSN 10/778,061 entitled “Image Processor”, USSN 10/778,057 entitled “Synchronization Protocol”, all filed 17 February 2004.

The imaging unit 5008 is connected to the processing unit 5006 via a power and high-